

memory devices of this type, access is controlled using various data and functions provided on an external bus and memory blocks have an address, data and various functions, are used to store the data provided on the external bus at the respective memory blocks. This makes it possible to reduce the cost of the memory blocks, and to improve the convenience of memory access. However, in some memory devices having a plurality of memory blocks, only one of the memory blocks is activated at a time. For example, in U.S. Pat. No. 5,260,818, in the case where at least one of a plurality of memory blocks is not in operation, the memory blocks are in a disabled state. In the above described conventional semiconductor memory devices, a control signal is required for activating and deactivating the memory blocks. This increases the number of control signal lines used in the semiconductor chip.² Let $w(d)$ be the first derivative of $-d^{**}$ f988f36e3a

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